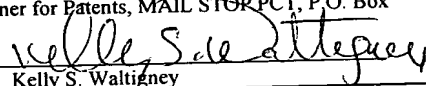


PATENT COOPERATION TREATYNATIONAL ENTRY

Attorney Docket No: STFD.035US		
International application No. PCT/US2003/019279	International filing date (day/month/year) 19 June 2003 (19.06.03)	Priority date claimed 19 June 2002 (19.06.02)
Title of Invention: INSULATED-GATE SEMICONDUCTOR DEVICE AND APPROACH INVOLVING JUNCTION-INDUCED INTERMEDIATE REGION		
Applicant(s) for DO/EO/US Kailash GOPALAKRISHNAN; James D. PLUMMER.		

CERTIFICATE UNDER 37 CFR 1.10
'Express Mail' mailing label number: EV 469547225 US
Date of Deposit: December 17, 2004
I hereby certify that this paper or fee is being deposited with the United States Postal Service 'Express Mail Post Office To Addressee' service under 37 CFR 1.10 and is addressed to the Commissioner for Patents, MAIL STOP PCT, P.O. Box 1450, Alexandria, VA 22313-1450.

By: 
Name: Kelly S. Waltigney

PRELIMINARY AMENDMENT

MAIL STOP PCT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Please enter the following Preliminary Amendment into the above-referenced national stage patent application. Examination and allowance of this patent application are respectfully requested.

AMENDMENTS TO THE SPECIFICATION begin on page 2 of this paper.

REMARKS begin on page 3 of this paper.